

Belegung TC2030–ALT

Tag–Connect stellt für den TC2030–ALT kein eigenes Datenblatt bereit, sondern verweist auf das des TC2030–IDC. Die folgenden Seiten sind deshalb die Zeichnung des TC2030–IDC – für Maße, Toleranzen und den Freihaltebereich gilt sie unverändert.

Die Belegung des TC2030–ALT weicht davon ab:

Tag–Connect (Pattern)	IDC–Stecker (Debuggerseite)	Signal
1	4	Vref
2	5	TMS
3	9	TDI
4	1	TCLK
5	2 und 10	GND
6	3	TDO

Ziehen Sie die Pin–Nummern der folgenden Seiten NICHT für die Belegung heran. Im Zweifel fragen Sie uns – wir klären das für Ihren Aufbau.

TC2030–ALT connections

Tag–Connect does not publish a separate datasheet for the TC2030–ALT; it refers to the TC2030–IDC. The following pages are therefore the TC2030–IDC drawing – it applies unchanged for dimensions, tolerances and the keep–out area.

The TC2030–ALT connections differ:

Tag–Connect (pattern)	IDC connector (debugger side)	Signal
1	4	Vref
2	5	TMS
3	9	TDI
4	1	TCLK
5	2 and 10	GND
6	3	TDO

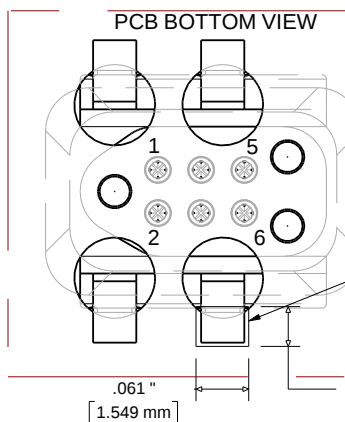
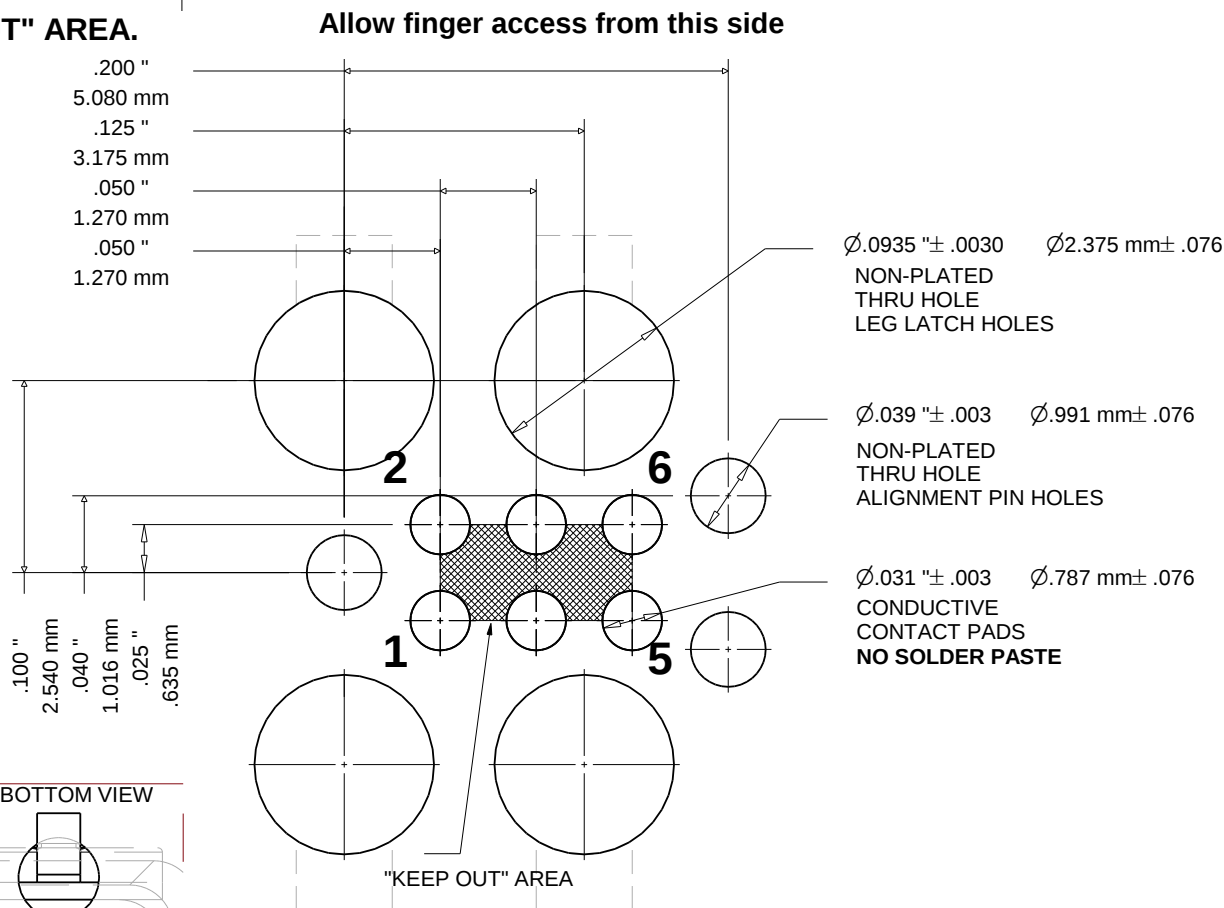
Do not use the pin numbers on the following pages for the pinout. If in doubt, ask us – we will sort it out for your assembly.

Notes:

1. Allow clearance for finger squeeze access from the two sides shown.
2. No tracks or vias in the shaded "KEEP OUT" AREA.
3. No other track or signal within .02" of any contact pad.
4. Do not allow solder paste on the contact pads. It is essential to eliminate any hole in the solder paste-mask (solder stencil) layer. If this is not possible make the contact pads thru-hole with a finished hole size of .008" or less.
5. Leg and alignment holes may be plated and used as vias but ensure minimum finished hole size meets specified tolerances.
6. To prevent purchasing confusion, please specify DNL in your BOM.
7. If using a proto-board fab that plates all through holes, make sure all holes are defined as plated in the footprint.

FOOTPRINT FOR TC2030-IDC PLUG-OF-NAILS™ CABLE

ZONE	REV	TC2030-IDC-FP	12/05/19	MS/NS
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Allow finger access from this side

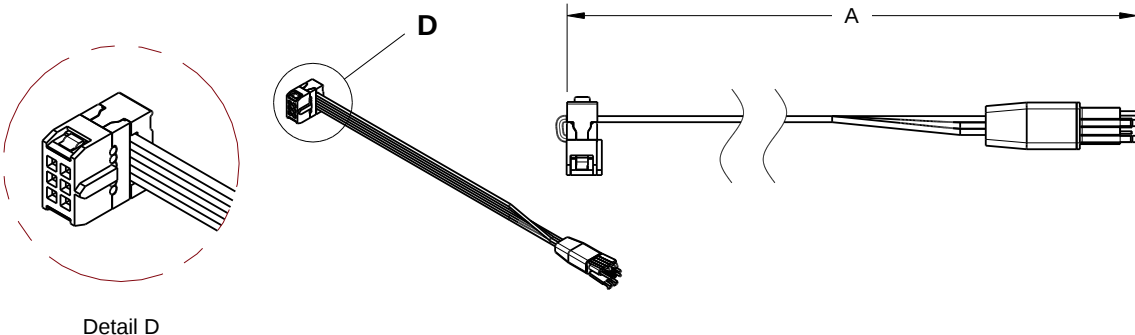
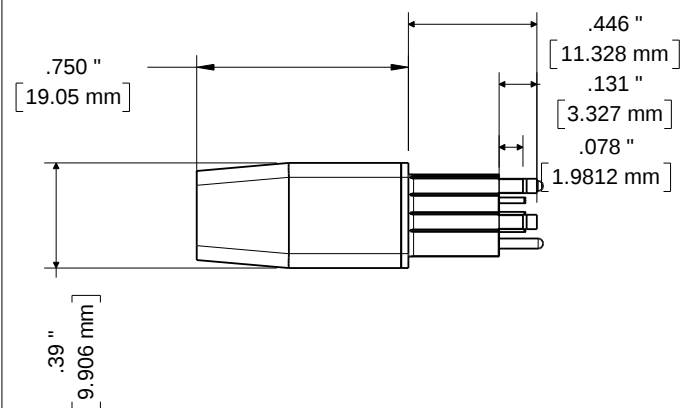
ALLOW A .061 X .046
SQUARE AREA OF
CLEARANCE FOR
EACH FOOT

SIZE	FSCM NO.	DWG NO.	FOOTPRINT FOR TC2030-IDC-FP	REV
A				B
SCALE	www.TAG-CONNECT.com		SHEET 1 of 3	

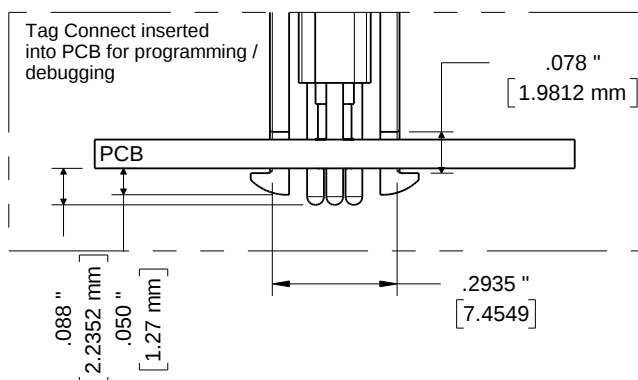
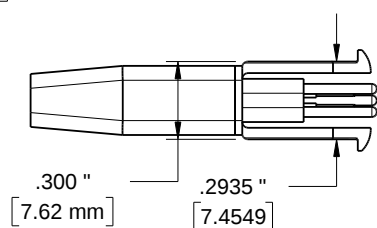
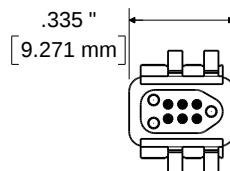
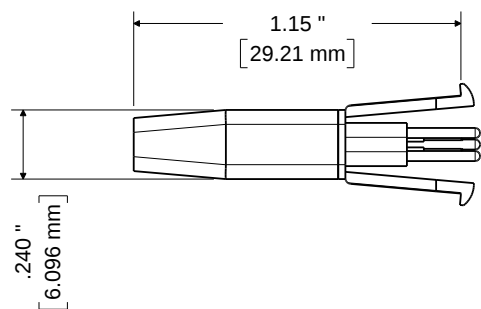
NOTE:
Dimensions subject to change

TC2030-IDC PLUG-OFF-NAILS™ CABLE

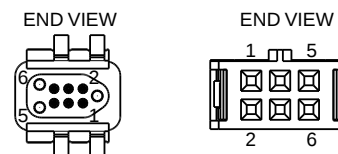
ZONE	A	TC2030-IDC	5/17/11	MS
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Detail D



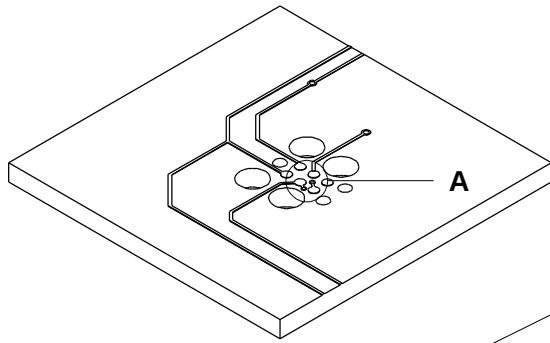
PART NUMBER	A	
	IN	MM
TC2030-IDC	8.8	225
TC2030-IDC-10	10.5	265



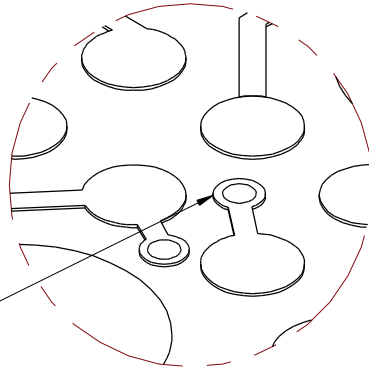
TAG 6 PIN	IDC 6pin
1	1
2	2
3	3
4	4
5	5
6	6

SIZE	FSCM NO.	DWG NO.	REV
A		TC2030-IDC	B
SCALE	www.TAG-CONNECT.com	SHEET 2 Of 3	

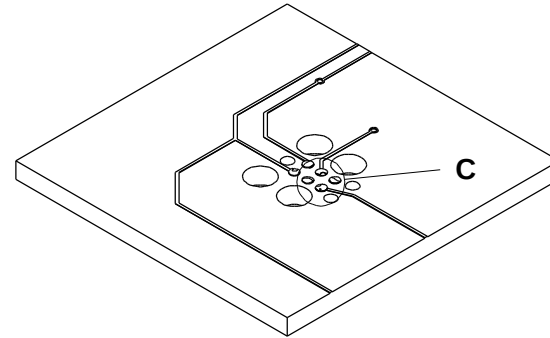
Layout Problems to Avoid



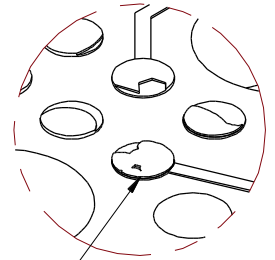
Vias between
contact pads



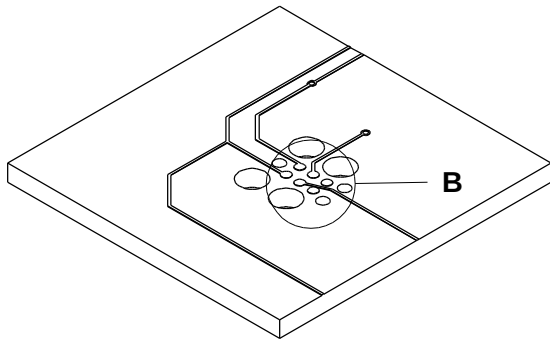
Detail A



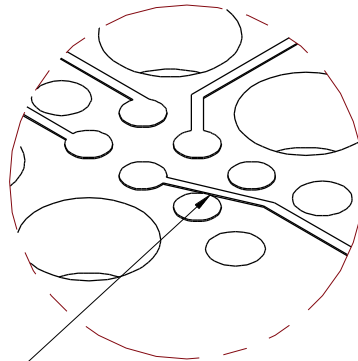
Do not deposit solder paste
on contact pads. View Solder
Paste Mask (Solder Stencil)
layer in pad-stack and eliminate
any hole.



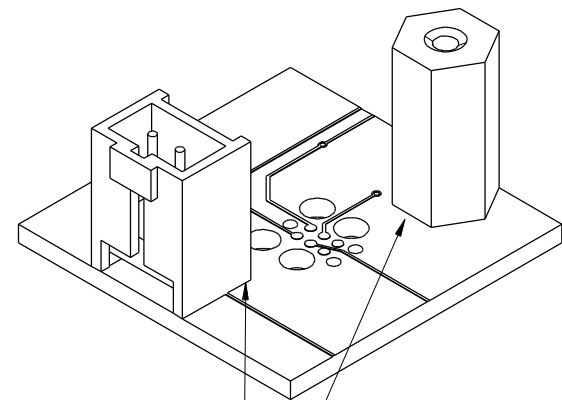
Detail C



Traces less than .02"
away from contact pad
or in "KEEP OUT" area



Detail B



Large components
blocking finger access to
the Tag-Connect leg clips